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April 25, 2007

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Examiner: L. Garbow

Art Unit: 2825

U.S. Patent Application Serial No.: 10/773,541

Filing Date: February 6, 2004 Inventor(s): Yutao MA et al.

Title: MODEL STAMPING MATRIX CHECK TECHNIQUE IN CIRCUIT

SIMULATOR

Docket No.: 188122000700

Papers enclosed herewith:

Transmittal (1 page) Amendment (9 pages)

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(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Yutao MA et al.

Application No.: 10/773,541

3,541 Confirmation No.: 6489

Filed: February 6, 2004

Art Unit: 2825

For: MODEL STAMPING MATRIX CHECK

TECHNIQUE IN CIRCUIT SIMULATOR

Examiner: L. Garbow

AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION

MS Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

This is in response to the non-final Office Action dated January 25, 2007 (Paper No./Mail Date 20070121), for which a response is due on April 25, 2007. Accordingly, this response is timely filed. Reconsideration and allowance of the pending claims, as amended, in light of the remarks presented herein are respectfully requested.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 7 of this paper.

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